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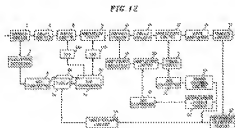
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A distortion compensation circuit reduces an unbalance between higher and lower 3rd order distortions, the distortions being generated by an amplifier which amplifies an input signal having at least two frequency components. The distortion compensation means, which includes an amplitude modulation means, which includes an amplitude detector, an amplitude circuit, a phase circuit and another amplitude circuit, generates sideband signals corresponding to an instantaneous phase difference between the higher and lower 3rd order distortions, the distortions that are generated by the amplifier can be cancelled. Further, sideband signal generating means, which includes an amplitude circuit, a delay circuit, an AM modulator, another amplitude circuit, another delay circuit and a PM modulator, performs AM and PM modulation on the input signal to generate sideband signals, the sideband signals and the sideband signals that are generated by the amplifier can be cancelled.



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[57] 摘要

一种失真补偿电路，可以降低由放大器所产生的高三阶失真和低三阶失真之间的不平衡，该放大器放大具有至少两个频率分量的输入信号。幅度调制单元包括一个幅度检波器、一个幅度电路、一个相位电路和另一个幅度电路，它产生对应于高三阶和低三阶失真之间瞬时相差的边带信号，从而能够消除该放大器所产生的失真。而且，边带信号产生单元包括一个幅度电路、一个延迟电路、一个 AM 调制器、另一个幅度电路、另一个延迟电路和一个 PM 调制器，它对输入信号执行 AM 和 PM，由此产生边带信号，从而能够消除该放大器所产生的失真。

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